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LSI Logic Corporation Legal Department – IP 1621 Barber Lane, MS D-106 Milpitas, CA 95035

Number of pages including cover sheet 2	_		
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REMARKS:		·	
	our review	Reply ASAP	☐ Please comment
Application Number: 10/665.927 Filing date: September 17, 2003 First named inventor: Alexander Tetelbau Attorney docket number: 02-5799 / 81604  Transmitted herewith for filing via facsimile:  • Status Request Letter	_		
Pursuant to 37 C.F.R. 1.8, I hereby certify that the Patent and Trademark Office on the date indicate	ed below:	is being transm	
Date	Signature	·	el Castillo

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

§ §

In re Application of:

Alexander Tetelbaum

Serial No.:

10/665,927

Filed:

September 17, 2003

For:

Method of Noise Analysis And

Correction Of Noise Violations For

An Integrated Circuit Design

Group Art Unit: 3819

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Examiner:

SEP 2 3 2004

Atty Docket:

81604 / 02-5799

## **STATUS REQUEST**

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Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 [±1] 408-433-7191

Date: 9/Moy

Respectfully submitted,

Sandeep Jaggi, Ph: [+1] 408-433-7191

Reg. No. 43,331